

Abstract

A shift register is provided, which adopts a shift operation delay for each memory device, or a data conversion control system through the estimation of conversion of data storage state. A driver circuit of LCD is provided, which adopts such a shift register, to thereby prevent instantaneous increase of electric power consumption while preventing EMI.

Accordingly, shift registers operate as being sequentially delayed for each memory device or data conversion is minimized, thus preventing instantaneous excessive consumption of electric power.

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